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				Application Number	
				Filing Date	
				First Named Inventor	Chen et al.
				Group Art Unit	
Examiner Name					
Sheet	1	of	1	Attorney Docket No.	TI-36559

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
PA	AA	6,075,295		LI	06/13/2000	Entire Document
PA	AB	6,215,288	B1	Ramsey et al.	04/10/2001	Entire Document
PA	AC	6,222,352	B1	Lenk	04/24/2001	Entire Document
PA	AD	6,522,110	B1	Ivanov	02/18/2003	Entire Document
	AF					
	AG					

FOREIGN PATENT DOCUMENTS							
Exam. Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Exam: Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
PA	CA	"High-Efficiency Multiple-Output DC-DC Conversion for Low-Voltage Systems", ABRAM P. DANCY, RAJEEVAN AMIRTHARAJAH and ANANTHA P. CHANDRAKASAN, IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 8, No. 3, June, 2000, pp. 252-263.		
PA	CB	"Single-Inductor Multiple-Output Switching Converters With Time-Multiplexing Control in Discontinuous Conduction Mode", DONGSHENG MA, WING-HUNG KI, CHI-YING TSUI and PHILIP K.T. MOK, IEEE Journal of Solid State Circuits, Vol. 38, No. 1, January, 2003, pp. 89-100.		
PA	CC	"23.1 A Synchronous Dual-Output Switching dc-dc Converter Using Multibit Noise-Shaped Switch Control", MARCUS W. MAY, MICHAEL R. MAY and JOHN E. WILLIS, ISSCC 2001/Session 23/ Analog Techniques, IEEE International Solid-State Circuits Conference, February 7, 2001, 2 pgs.		
	CD			
	CE			
	CF			
	CG			
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Examiner Signature		Date Considered	11/10/06
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